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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Grivna, Gordon M. et al.  
Serial No.: 10/773,853  
Filing Date: February 9, 2004  
Group Art Unit: 2826  
Examiner: Fetsum Abraham  
Title: SEMICONDUCTOR DEVICE HAVING REDUCED CAPACITANCE TO SUBSTRATE AND METHOD

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO: COMMISSIONER FOR PATENTS, P.O. Box 1450 Alexandria, VA 22313-1450, ON: November 8, 2004

Date of Deposit

Semiconductor Components Industries, LLC.

Name of Assignee

Lydia McNamee

11-8-04

SIGNATURE

DATE

AMENDMENT

Honorable Commissioner for Patents,  
P.O. Box 1450  
Alexandria, VA 22313-1450

SIR:

In response to the Office Action mailed September 22, 2004, please reconsider the above-identified patent application in view of the amendments and remarks presented hereinafter.